



Attorney's Docket No.: 42P17814

PATENT

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In Re Patent Application of:

Robert Chau et al.

Application No.: 10/760,028

Filed: January 16, 2004

For: TRI-GATE TRANSISTOR AND  
METHOD TO FABRICATE SAME

Examiner: Smith, Bradley

Art Unit: 2891

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

Sir:

Enclosed is a copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed). It is respectfully requested that the cited documents be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

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X 37 C.F.R. §1.97(b).

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\_\_\_\_\_ A statement pursuant to 37 C.F.R. §1.97(e) or

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If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 1/23, 2006

  
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Substitute for Form 1449/PTO  <div style="display: flex; align-items: center;"> <div> <h2 style="margin: 0;">INFORMATION DISCLOSURE STATEMENT BY APPLICANT</h2> <p style="margin: 0; font-size: small;">(use as many sheets as necessary)</p> </div> </div>				<b>Complete if Known</b>		
		Application Number	10/760,028			
		Filing Date	January 16, 2004			
		First Named Inventor:	Robert Chau et al.			
		Art Unit	2891			
		Examiner Name	Smith, Bradley			
		Attorney Docket Number	42P17814			
<b>U.S. PATENT DOCUMENTS</b>						
Examiner Initials*	Cite No. <sup>1</sup>	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)				
		US-	6,457,890 B1	11/05/2002	Yu.	
		US-	2004/0227187	11/18/2004	Cheng et al.	Fig. 9B-10C
		US-	5,124,777	06/23/1992	Lee, Sangin	
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<b>FOREIGN PATENT DOCUMENTS</b>							
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup>	Number <sup>4</sup> Kind Code <sup>5</sup> (if known)				
		EP 1 202 335 A2		05/02/2002	IBM Corporation		
		EP 1 566 422 A2		08/24/2005	Samsung Electronics		

Examiner Signature		Date Considered	
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup>Applicant's unique citation designation number (optional). <sup>2</sup>See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup>For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup>Applicant is to place a check mark here if English language translation is attached.

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<b>Substitute for Form 1449/PTO</b>  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(use as many sheets as necessary)</i>				<b>Complete if Known</b>	
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				Examiner Name	Smith, Bradley
Sheet	2	42P18257	2	Attorney Docket Number	42P17814

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published		T <sup>2</sup>
		T. Park et al., "Fabrication of Body-Tied FinFETs (Omega MOSFETS) Using Bulk Si Wafers", 2003 Symposia on VLSI Technology Digest of Technical Papers, June 2003, pages 135-136.		
		A. Burenkov et al., "Corner Effect in Double and Tripe Gate FinFets", IEEE 2003, ppg 135-138		
		S.T. Chang et al., "3-D Simulation of Strained Si/SiGe Heterojunction FinFETs", pp 176-177		
		International Search Report PCT/US2005/010505		
		International Search Report PCT/US2005/020339		

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